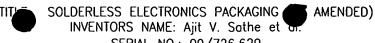


Fig. 1





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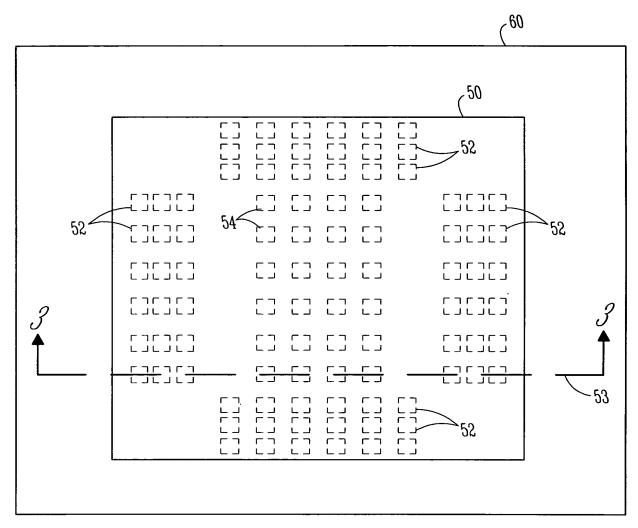


Fig.2 (Prior Art)





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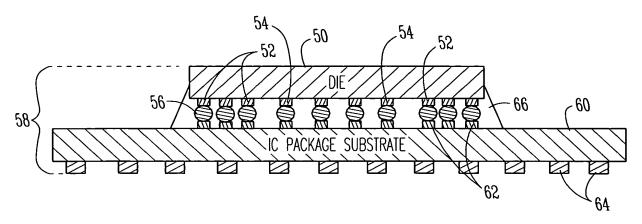


Fig. 3 (Prior Art)

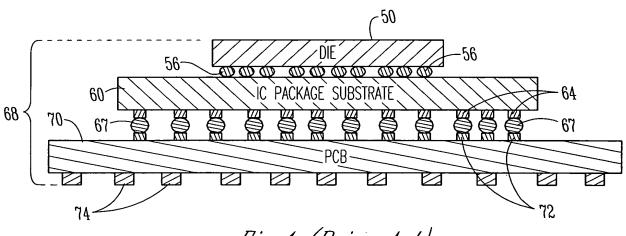


Fig. 4 (Prior Art)



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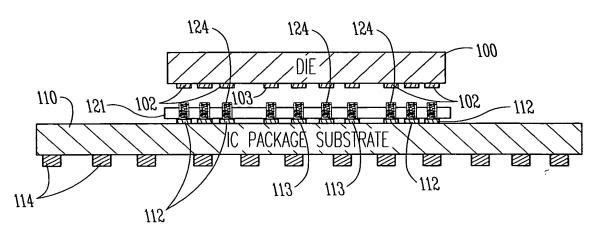
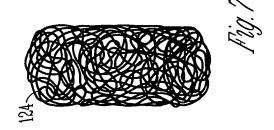


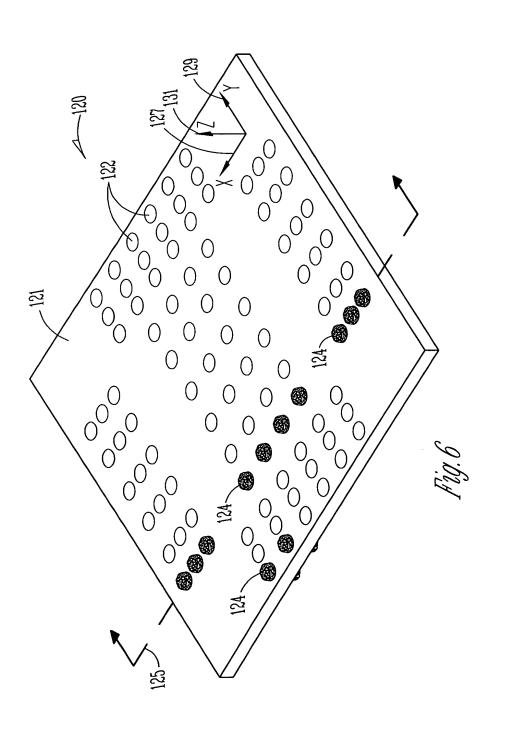
Fig. 5



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INVENTORS NAME: Ajit V. Sothe et al.

SERIAL NO.: 09/726,629



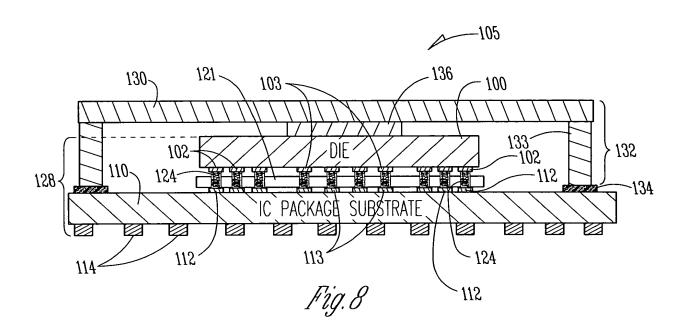






SOLDERLESS ELECTRONICS PACKAGING AMENDED)
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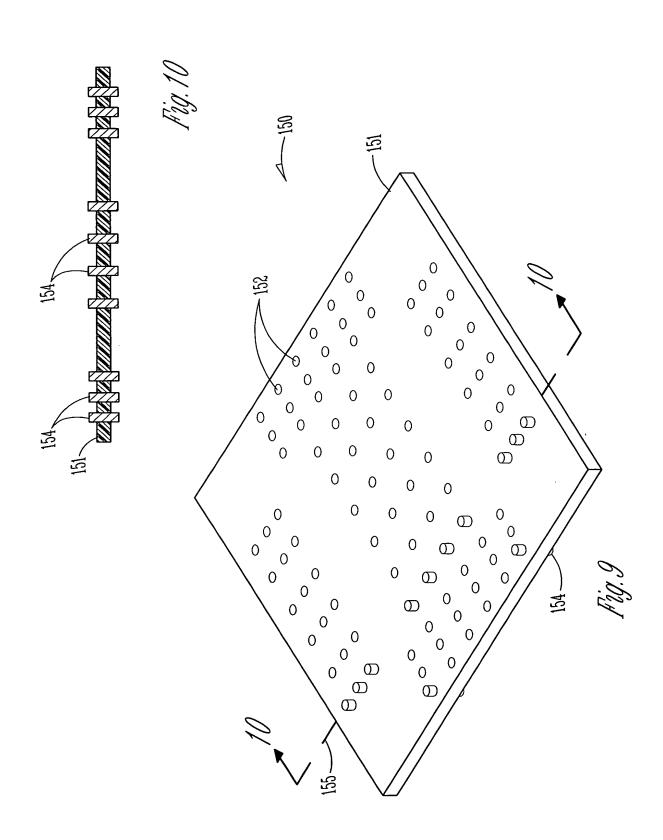
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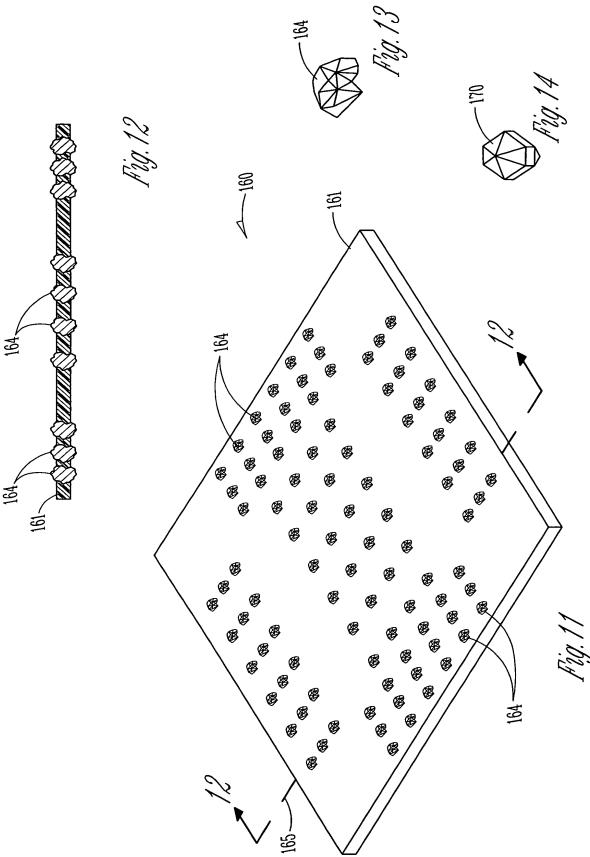


SOLDERLESS ELECTRONICS PACKAGING INVENTORS NAME: Ajit V. Sathe et di. SERIAL NO.: 09/726,629 AMENDED)

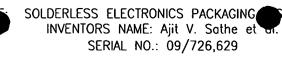




SOLDERLESS ELECTRONICS PACKAGING INVENTORS NAME: Ajit V. Sothe et di. SERIAL NO.: 09/726,629 AMENDED)

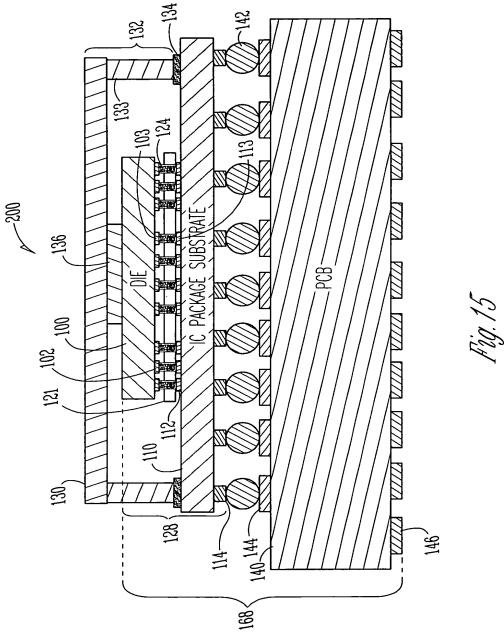






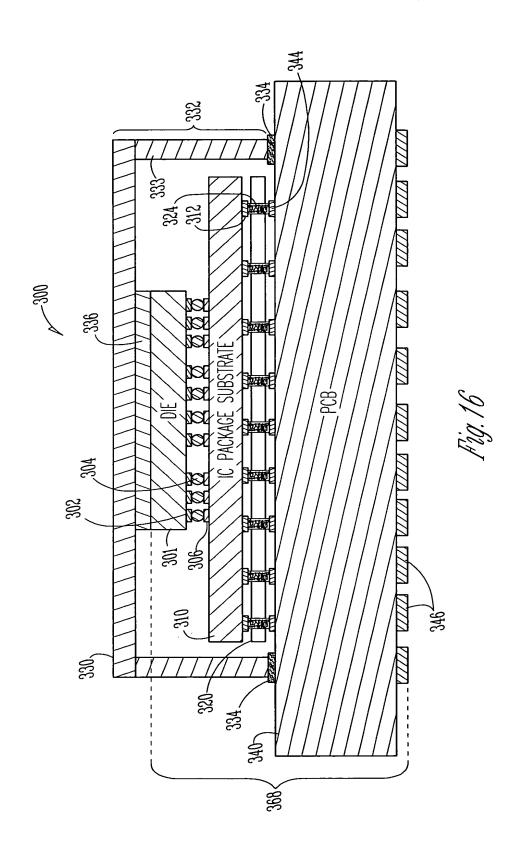
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AMENDED)





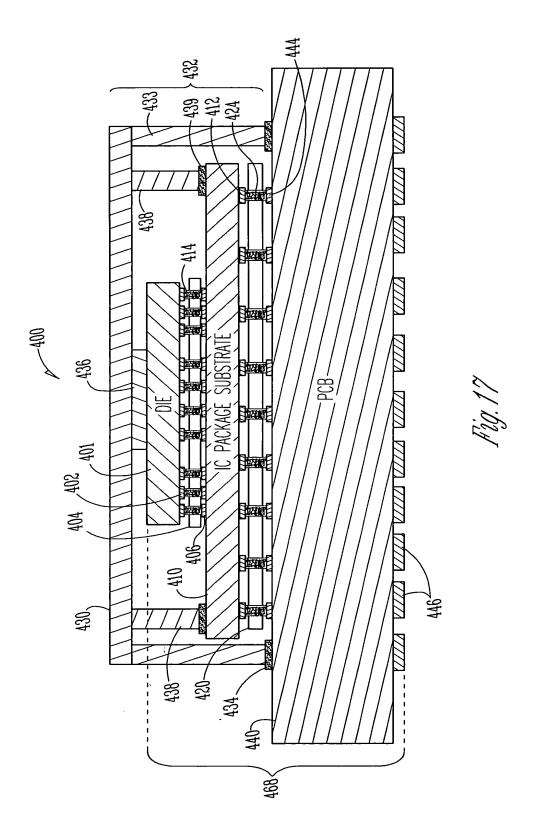
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SERIAL NO.: 09/726,629

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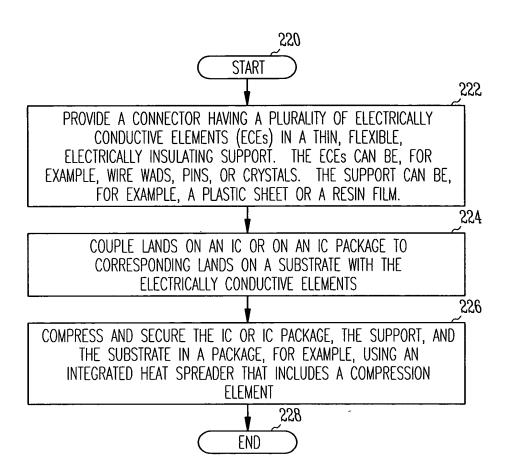


Fig. 18



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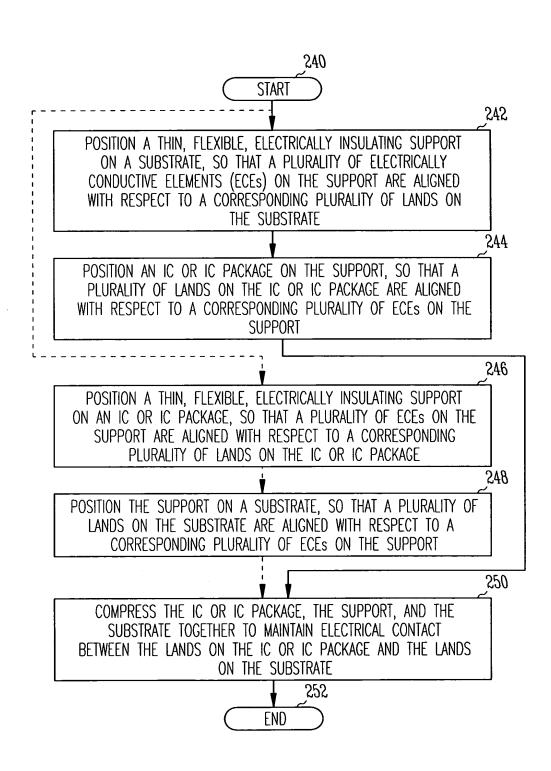


Fig. 19